

FIG. 1

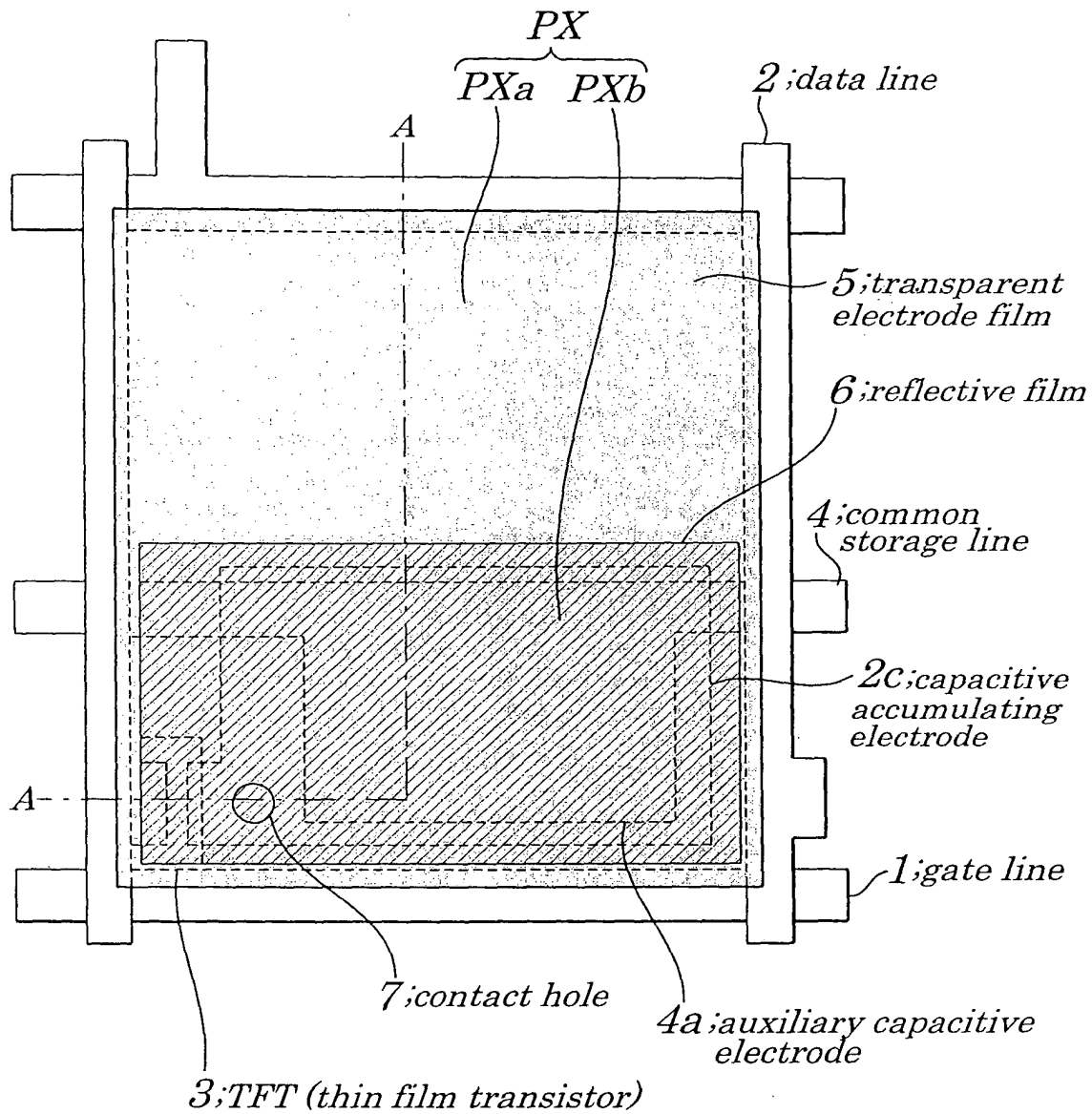
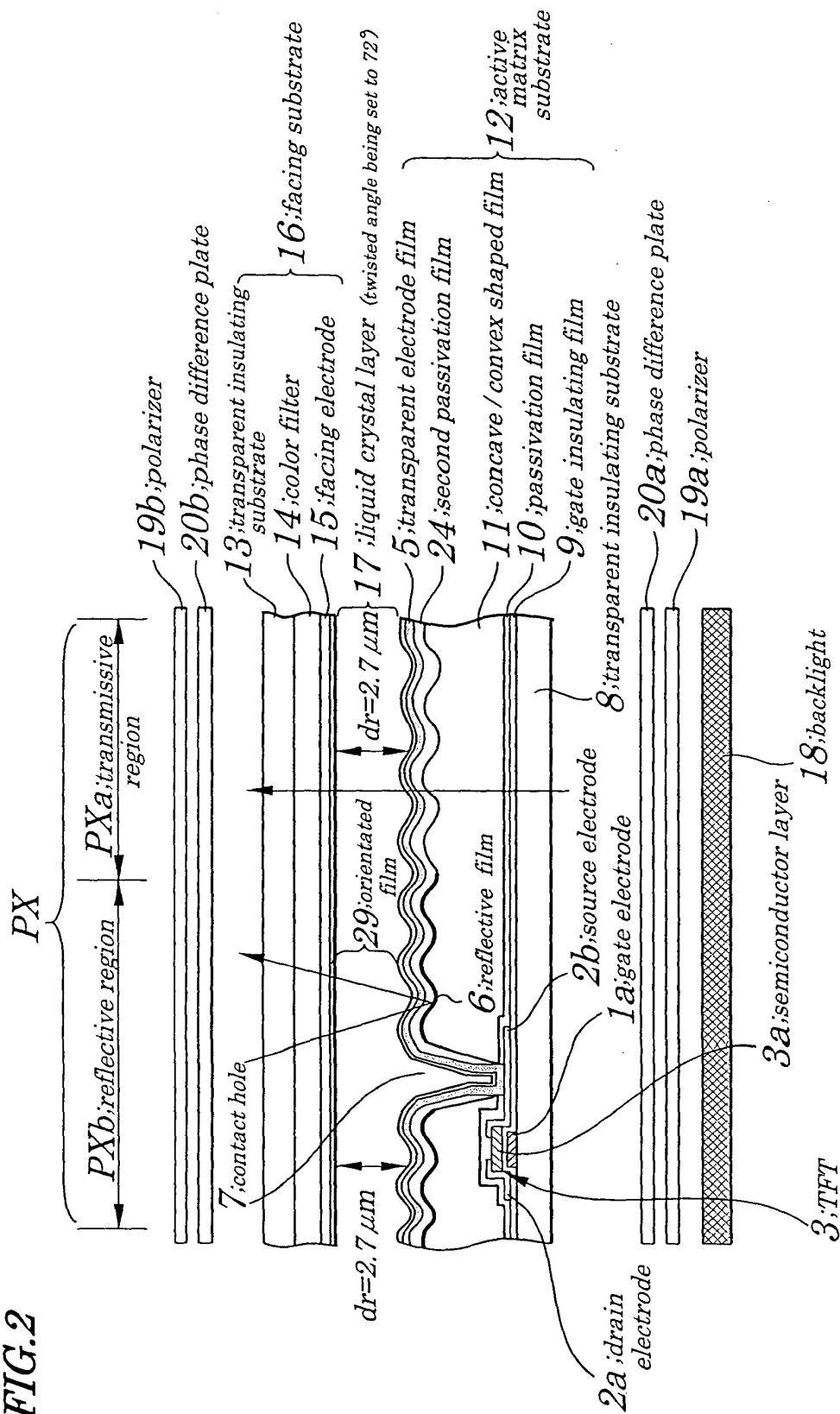
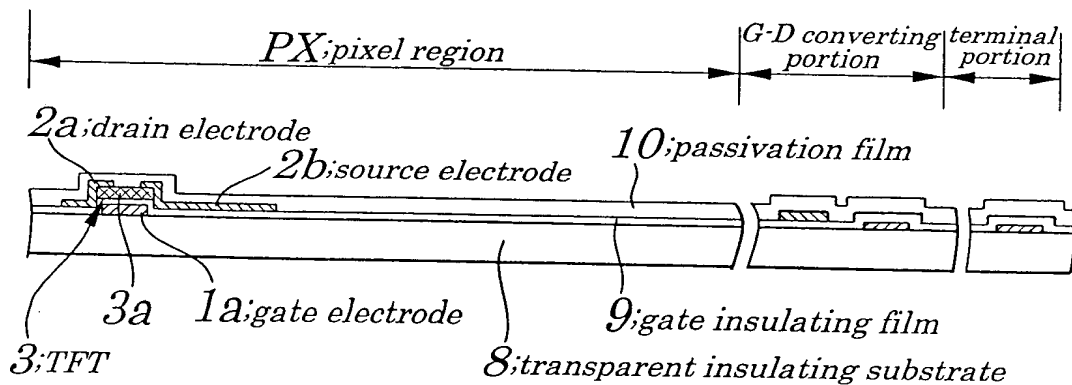


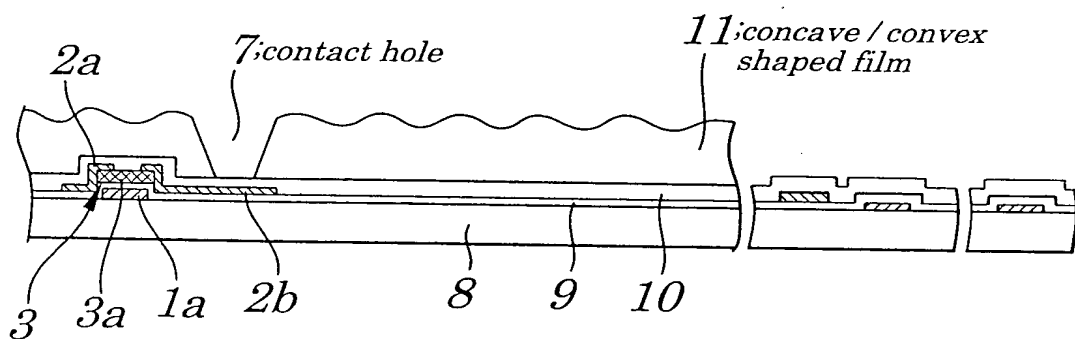
FIG. 2



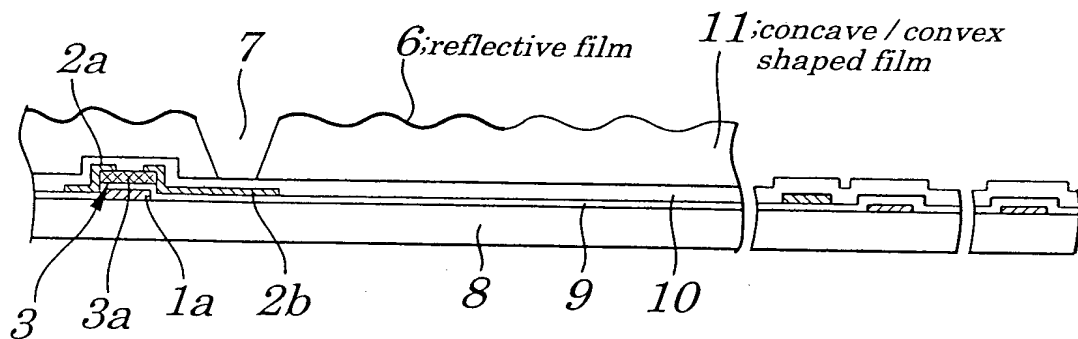
**FIG.3A**



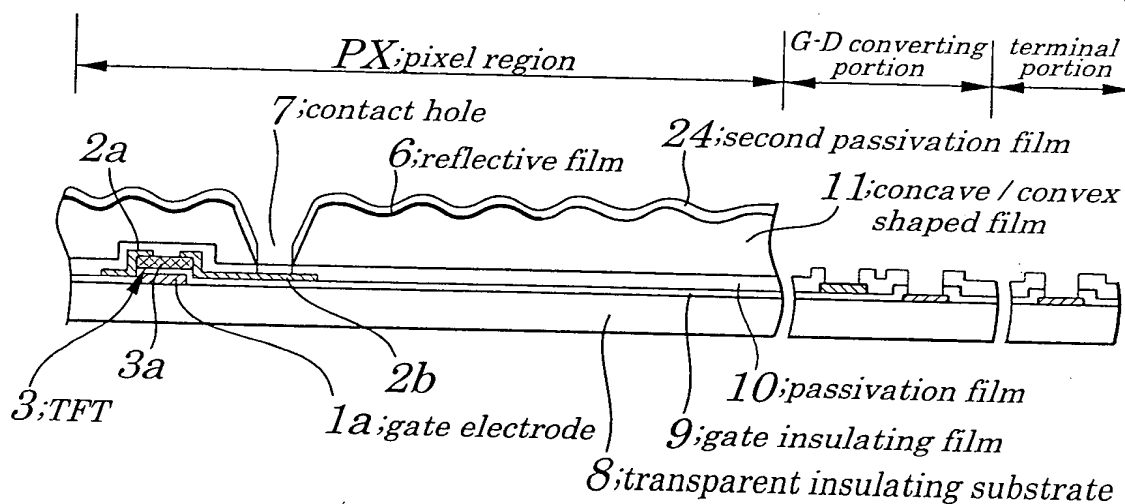
**FIG.3B**



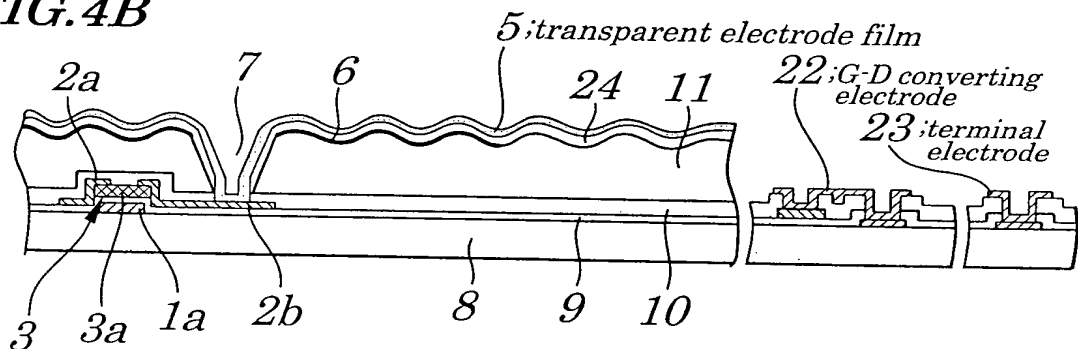
**FIG.3C**



**FIG. 4A**



**FIG. 4B**



**FIG. 5**

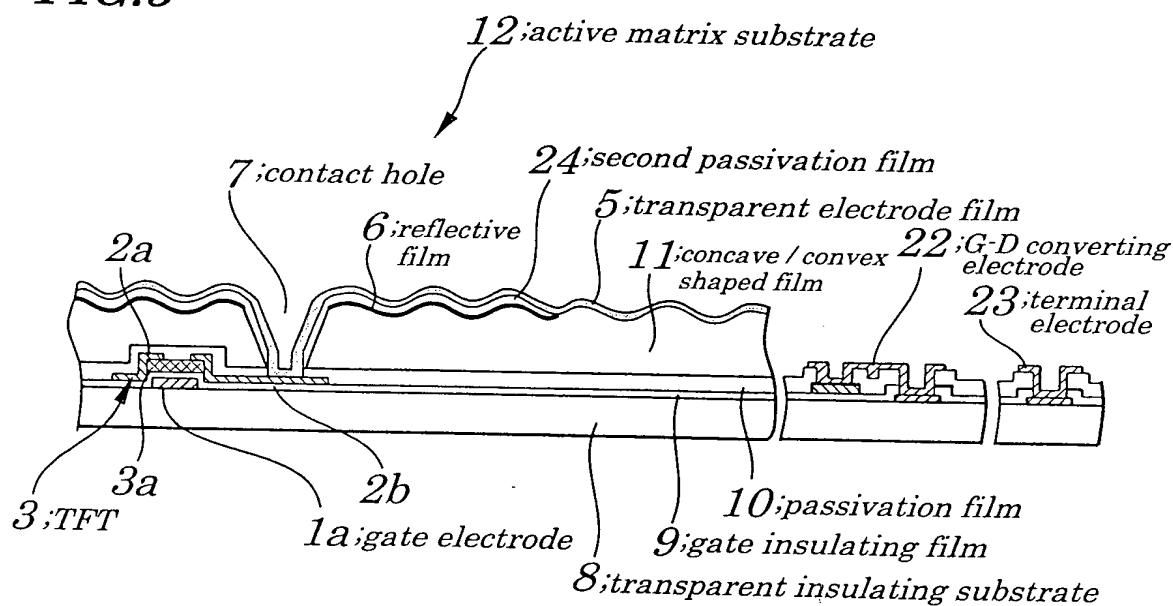


FIG. 6

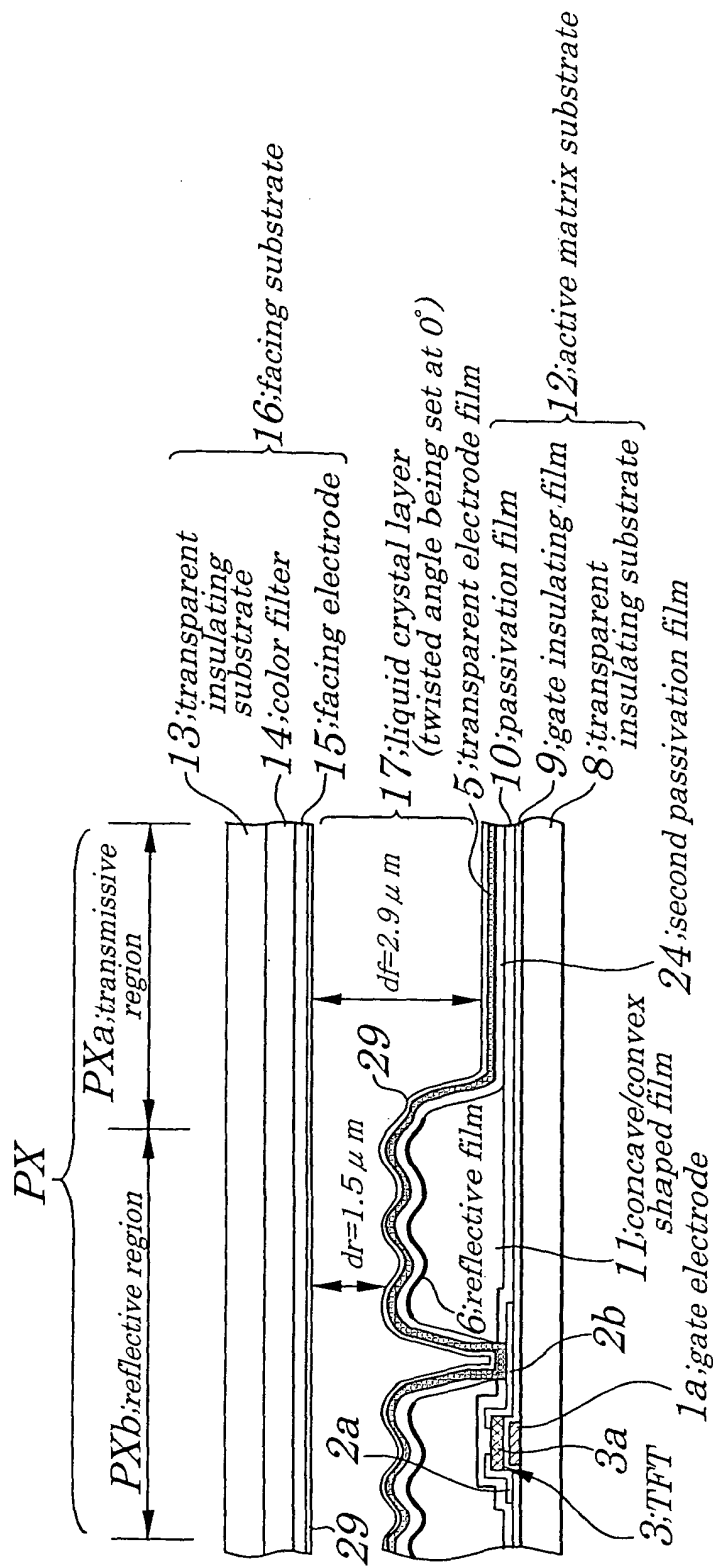


FIG. 7

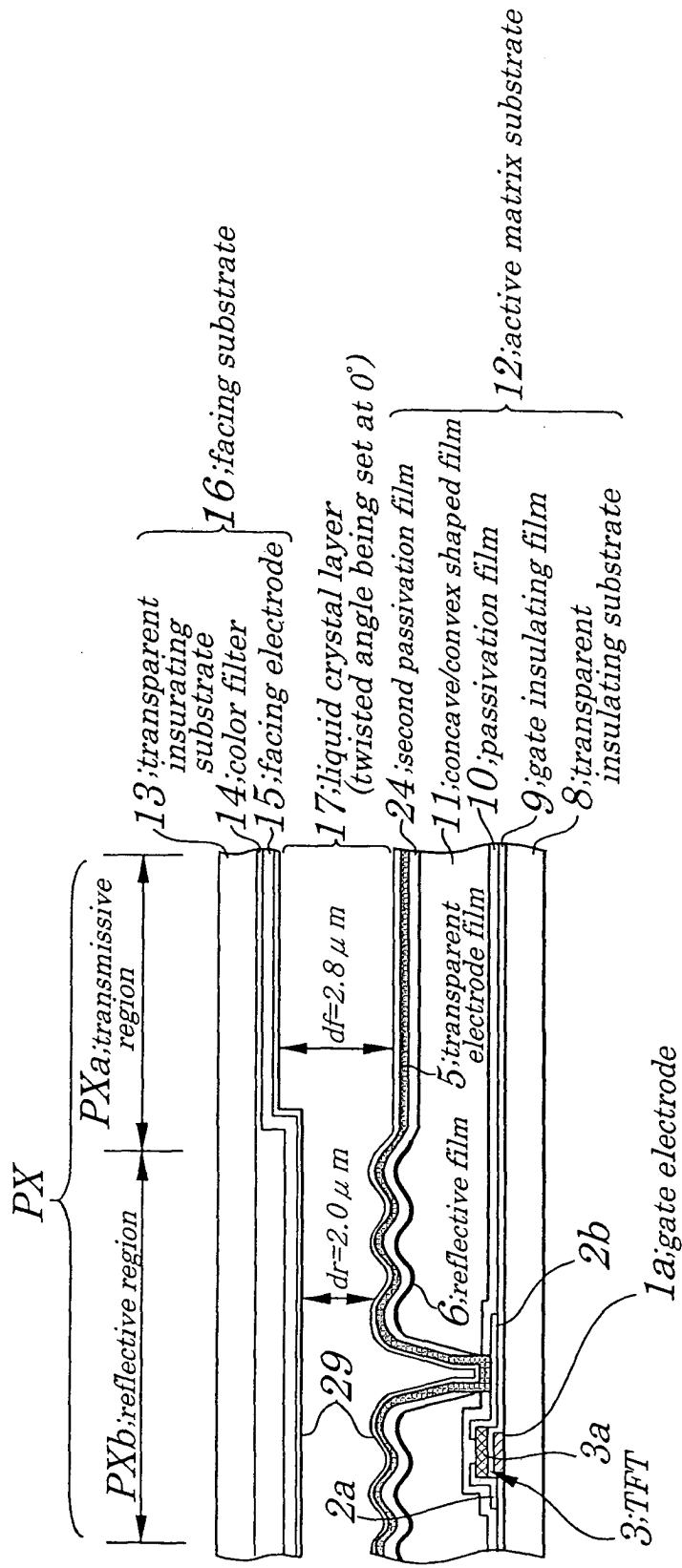
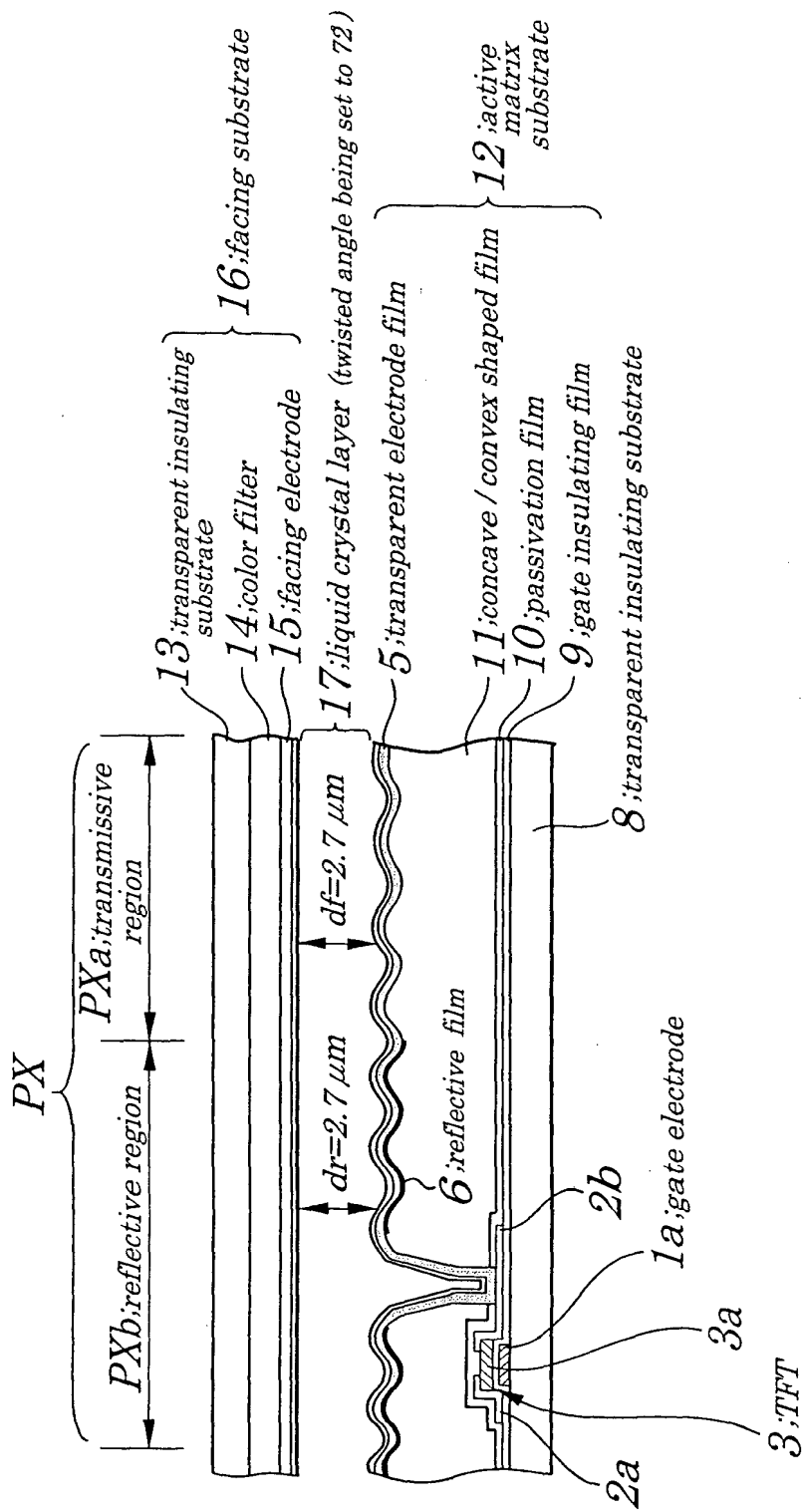
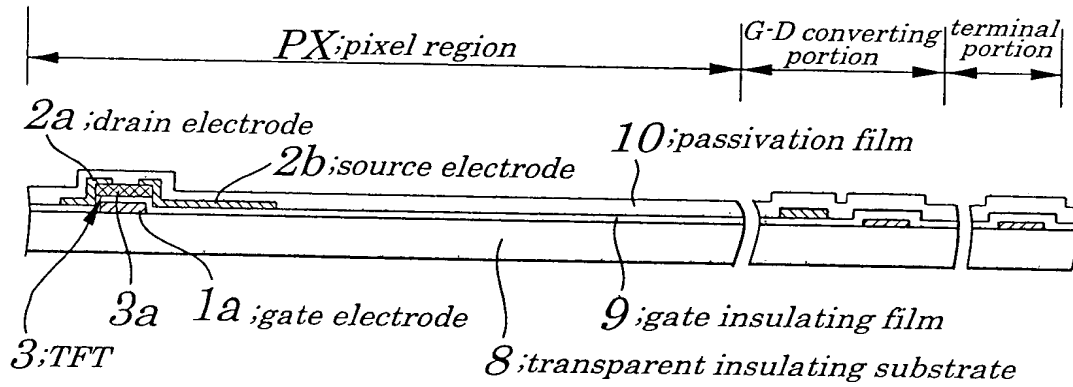


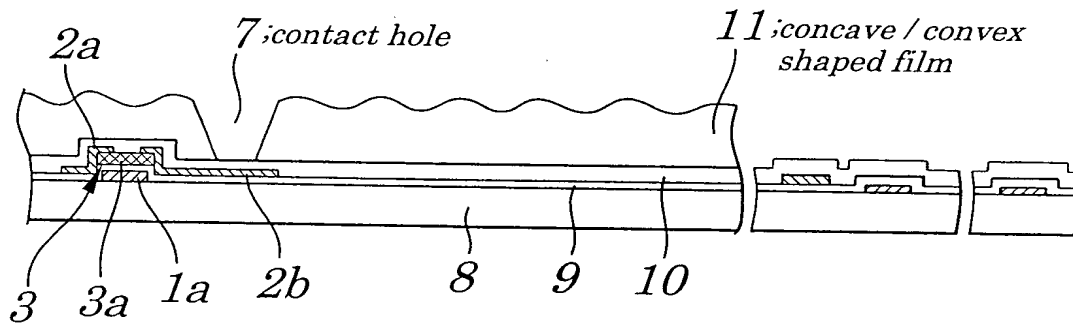
FIG. 8



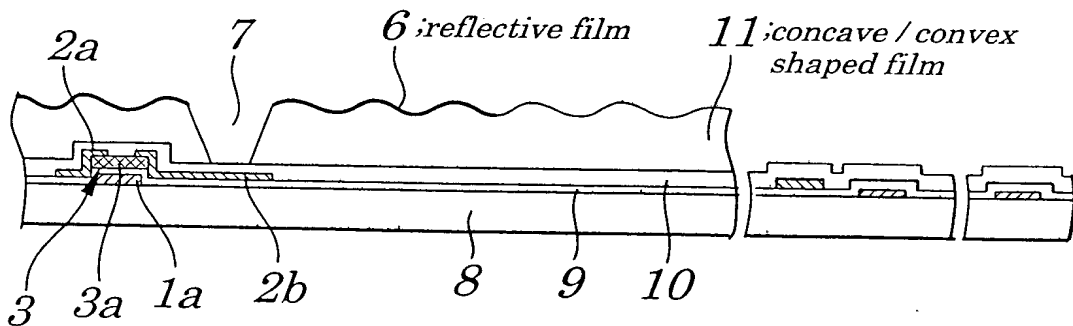
**FIG.9A**



**FIG.9B**

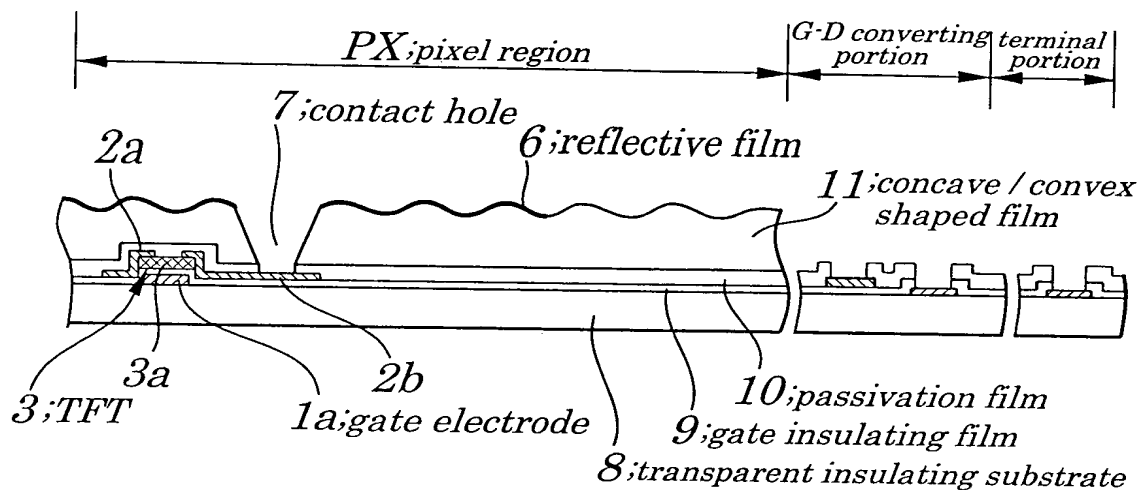


**FIG.9C**





**FIG. 10A**



**FIG. 10B**

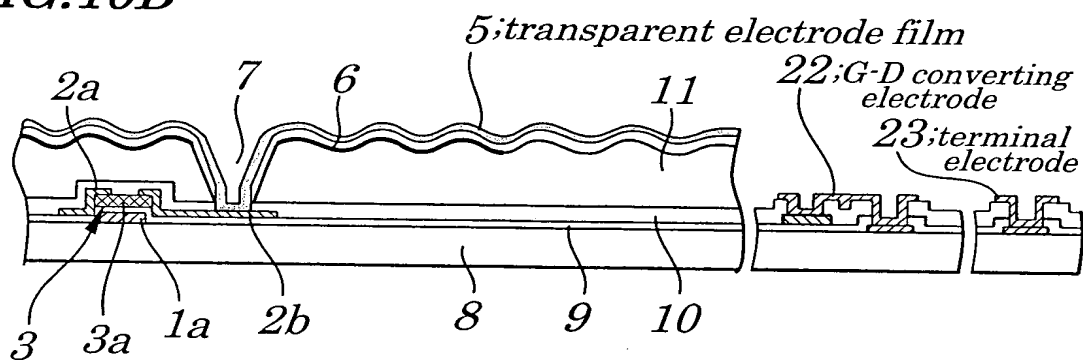


FIG. 11

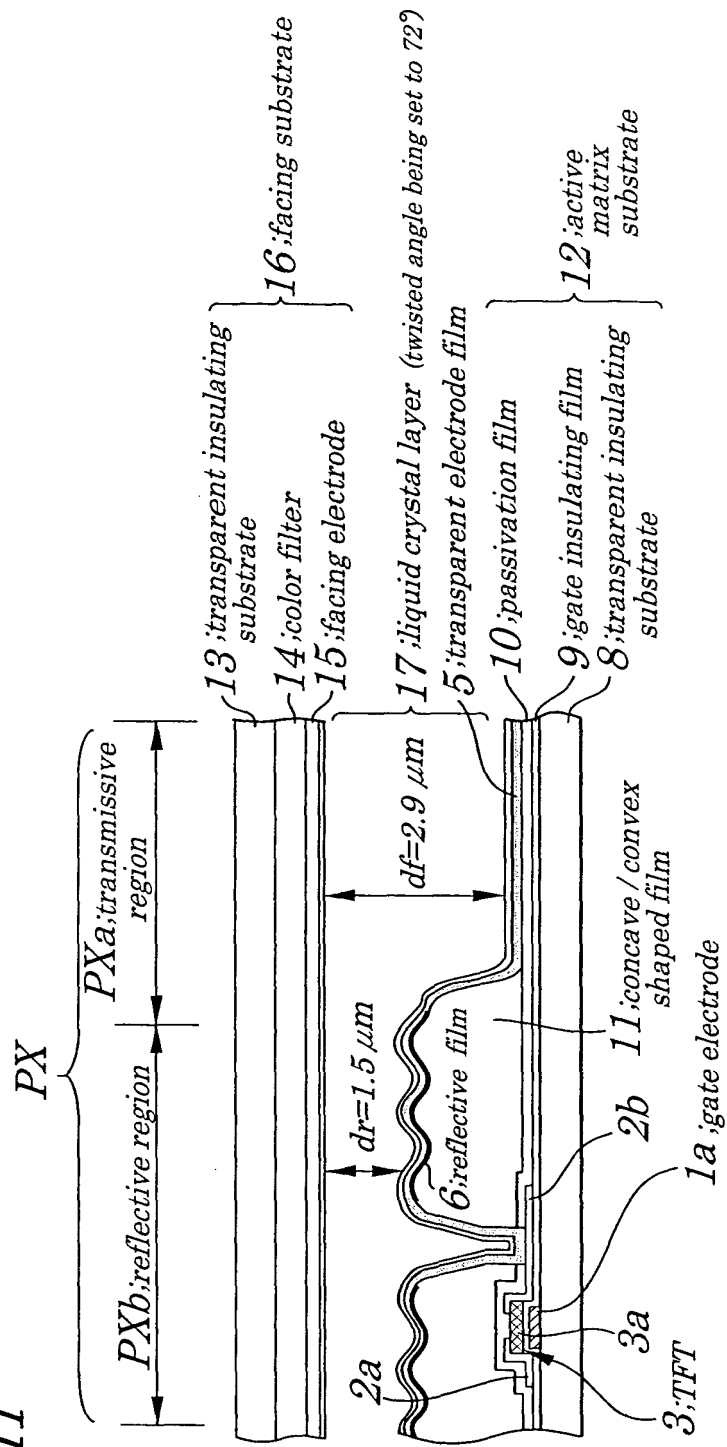
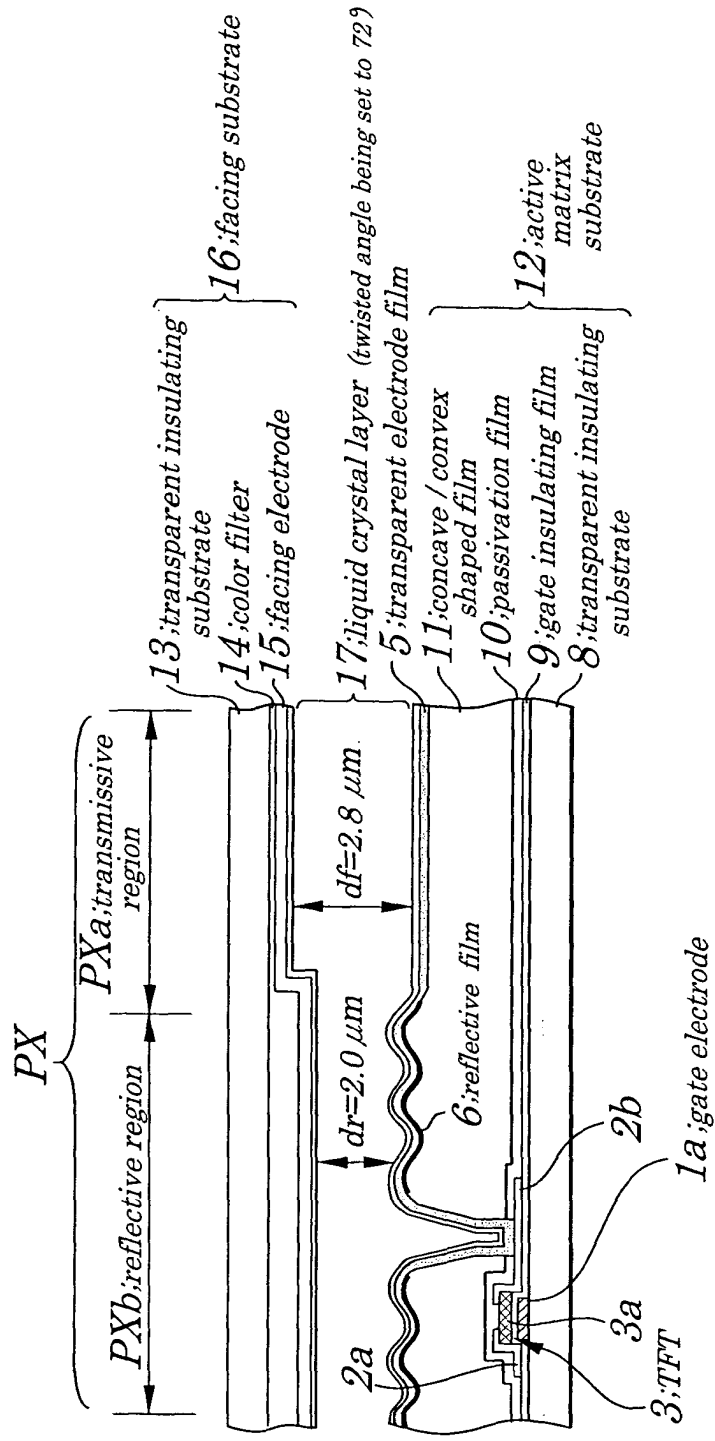
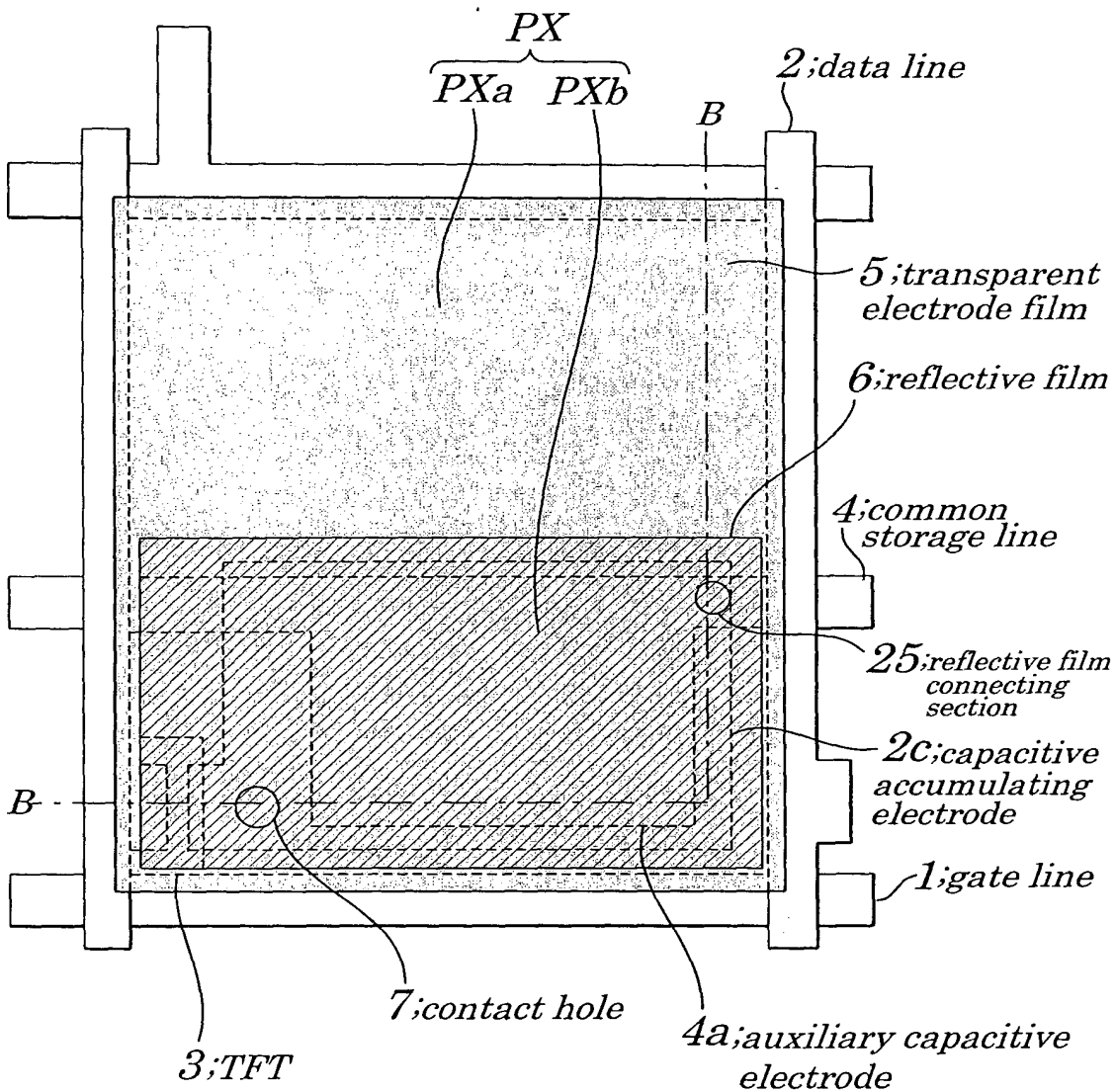


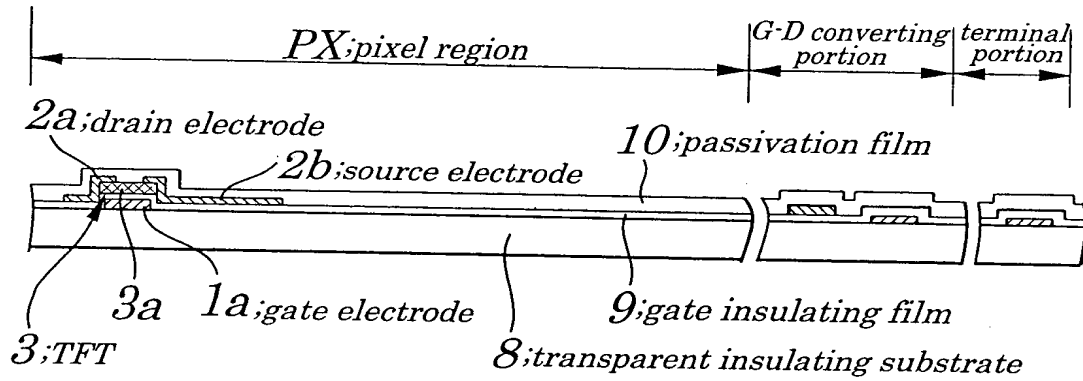
FIG. 12



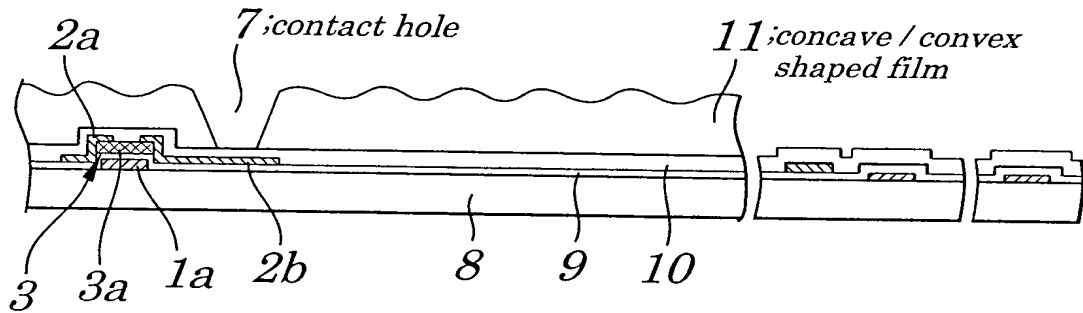
**FIG. 13**



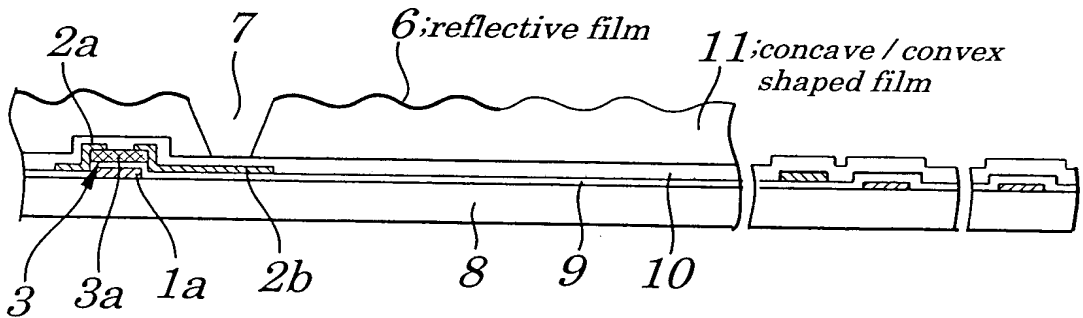
**FIG.14A**



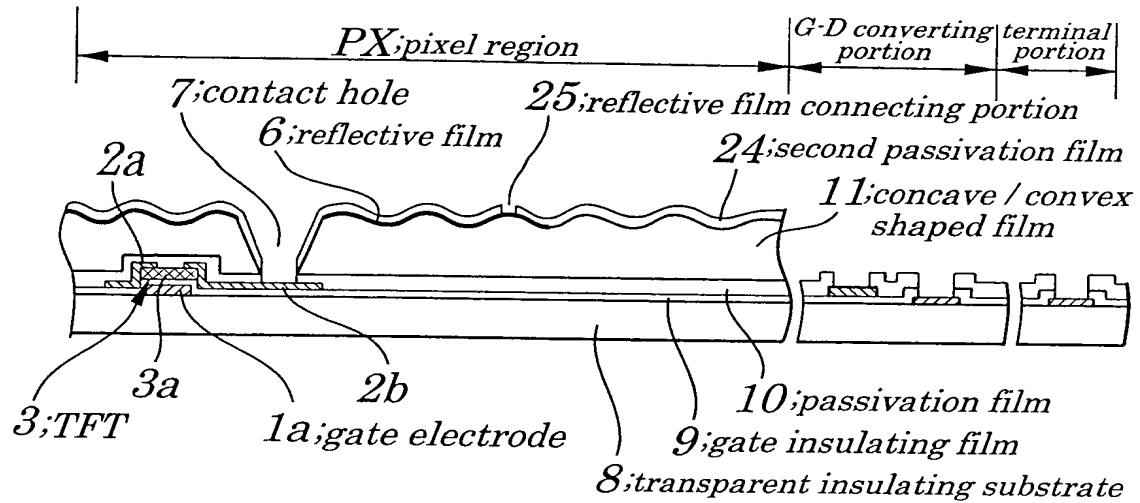
**FIG.14B**



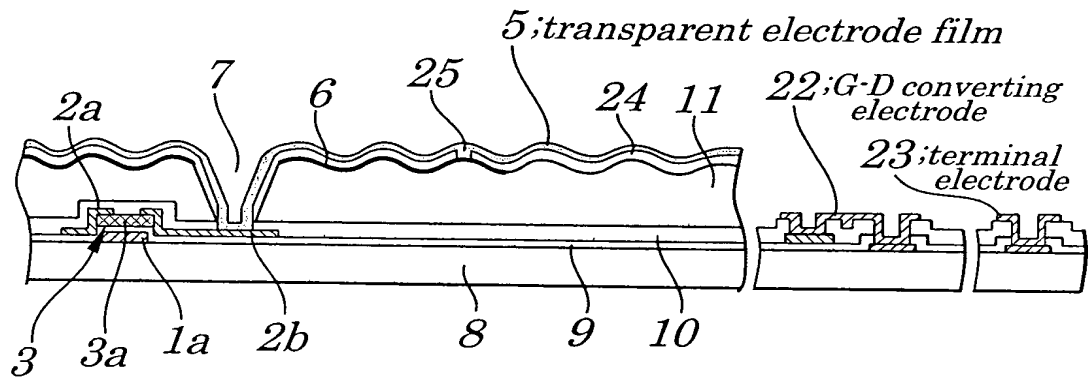
**FIG.14C**



**FIG. 15A**



**FIG. 15B**



**FIG.16**

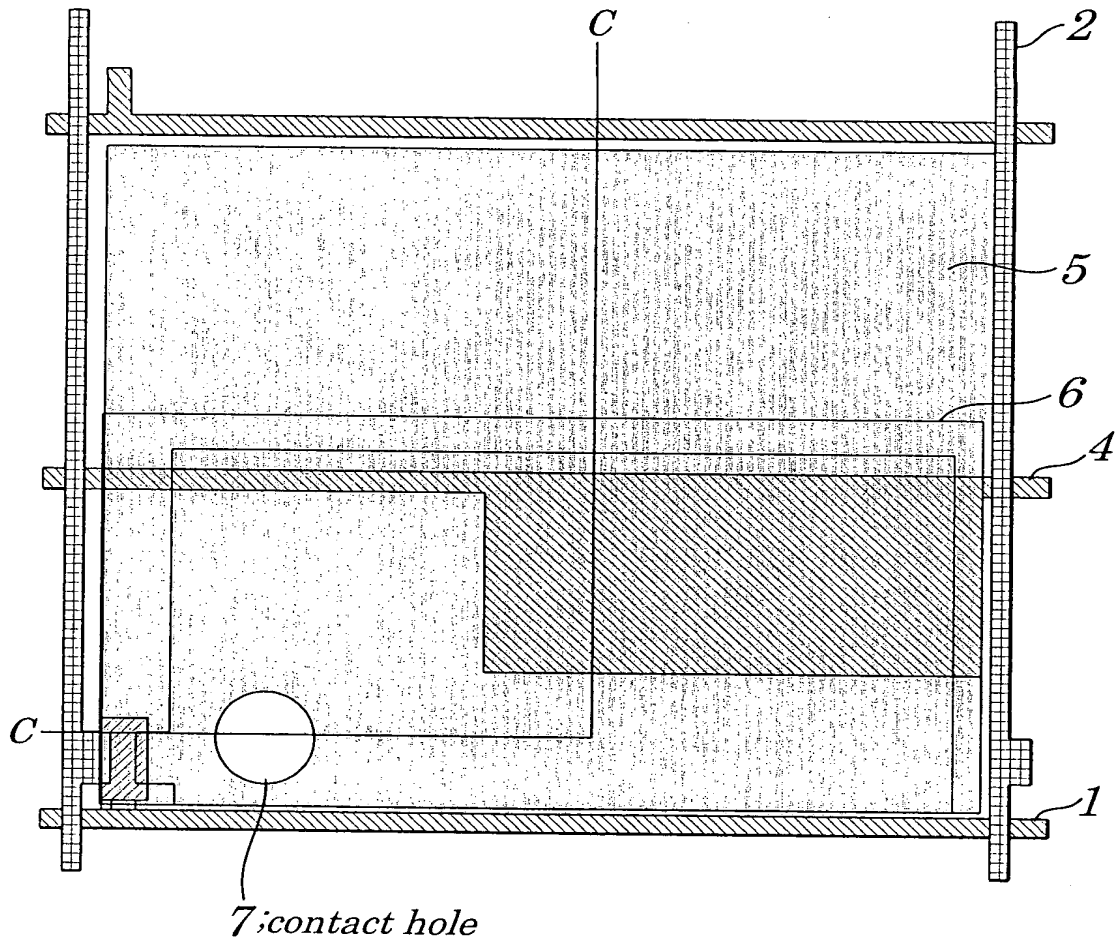
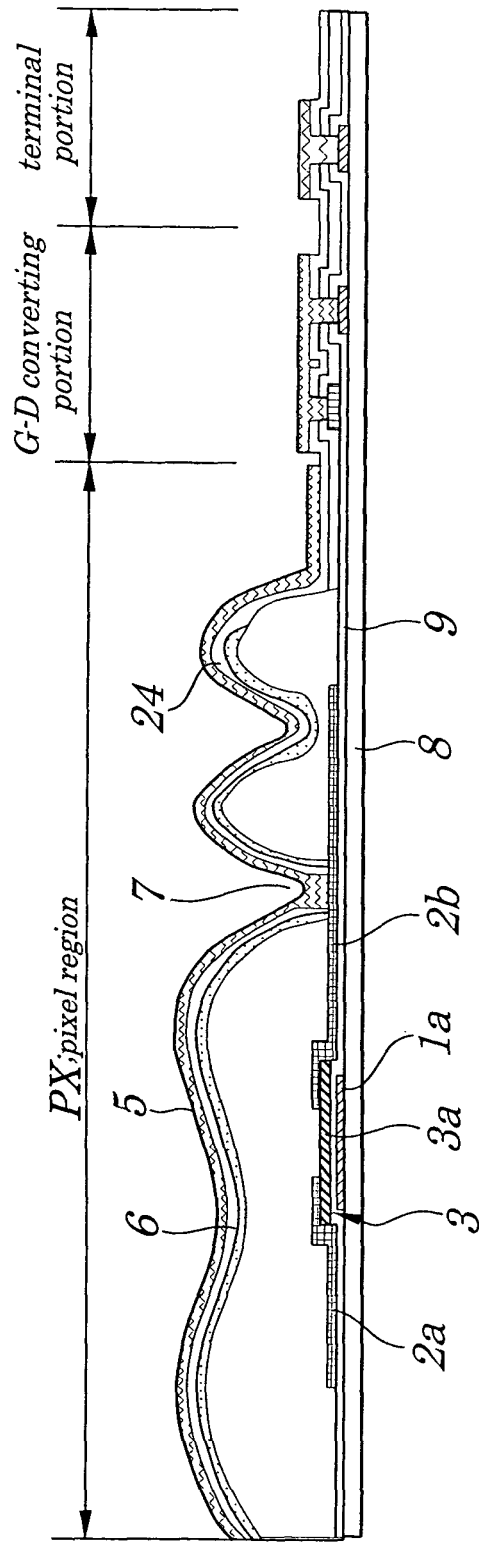


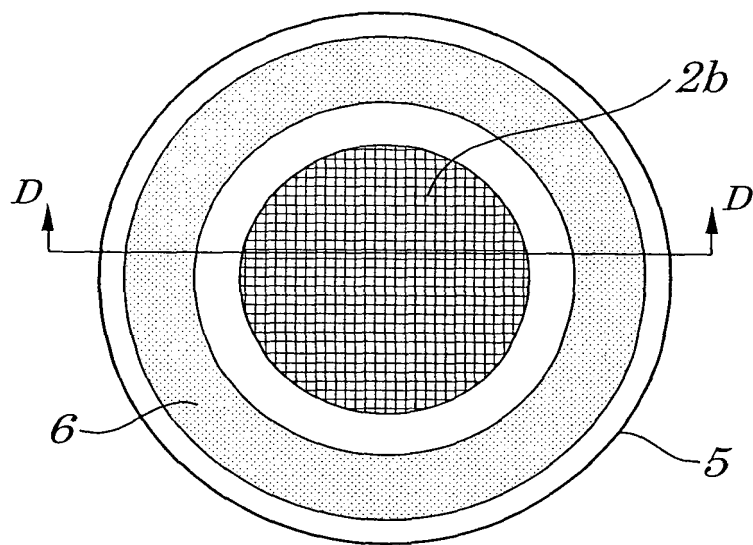
FIG.17







**FIG.18**



**FIG.19**

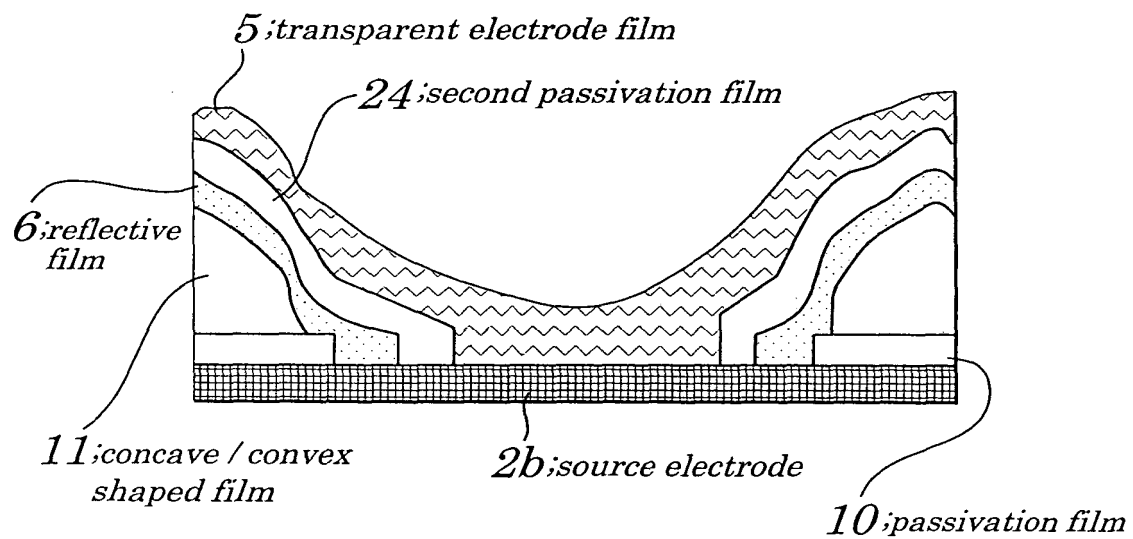


FIG. 20A

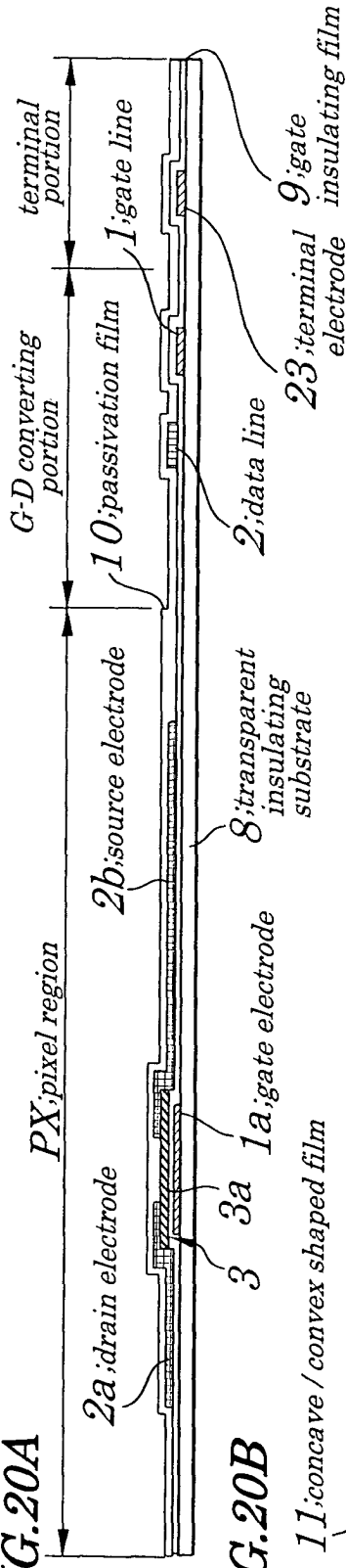


FIG. 20B

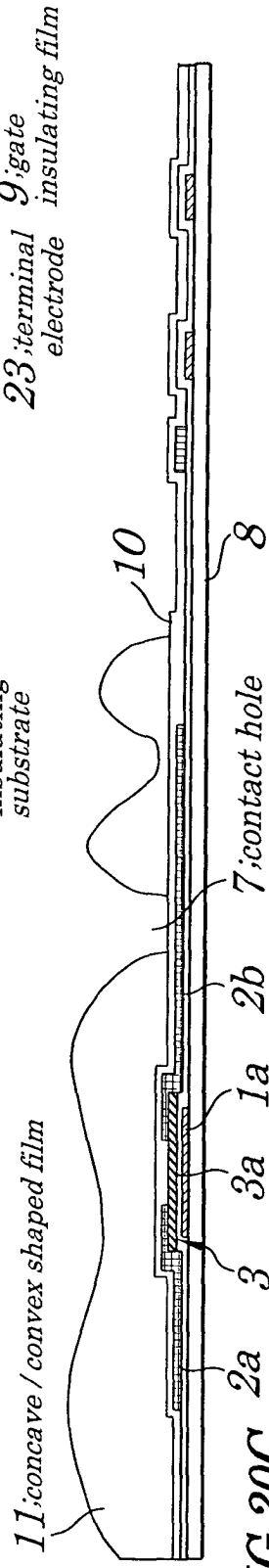


FIG. 20C

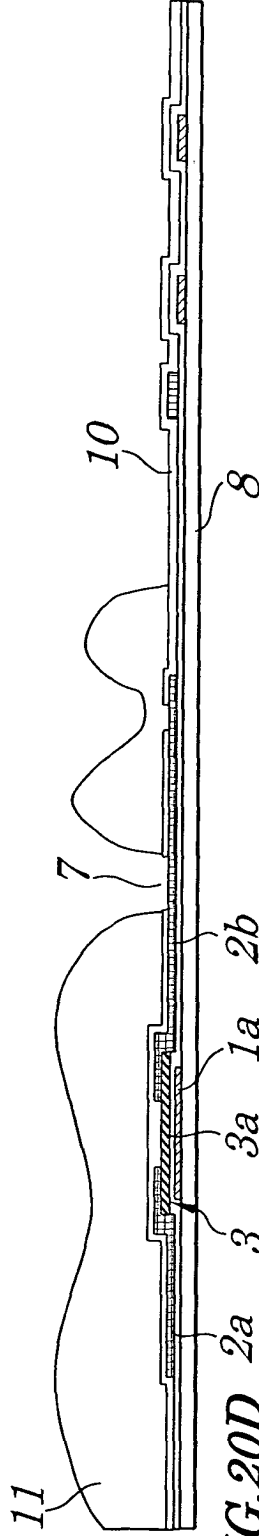


FIG. 20D

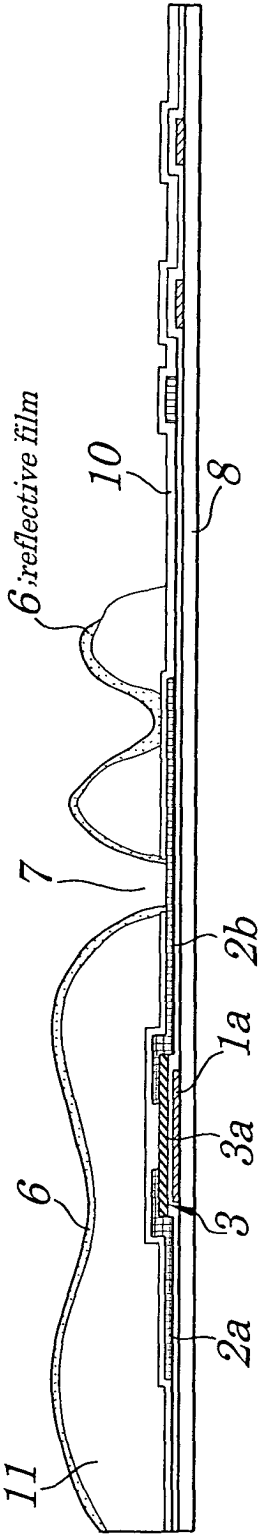


FIG. 21A

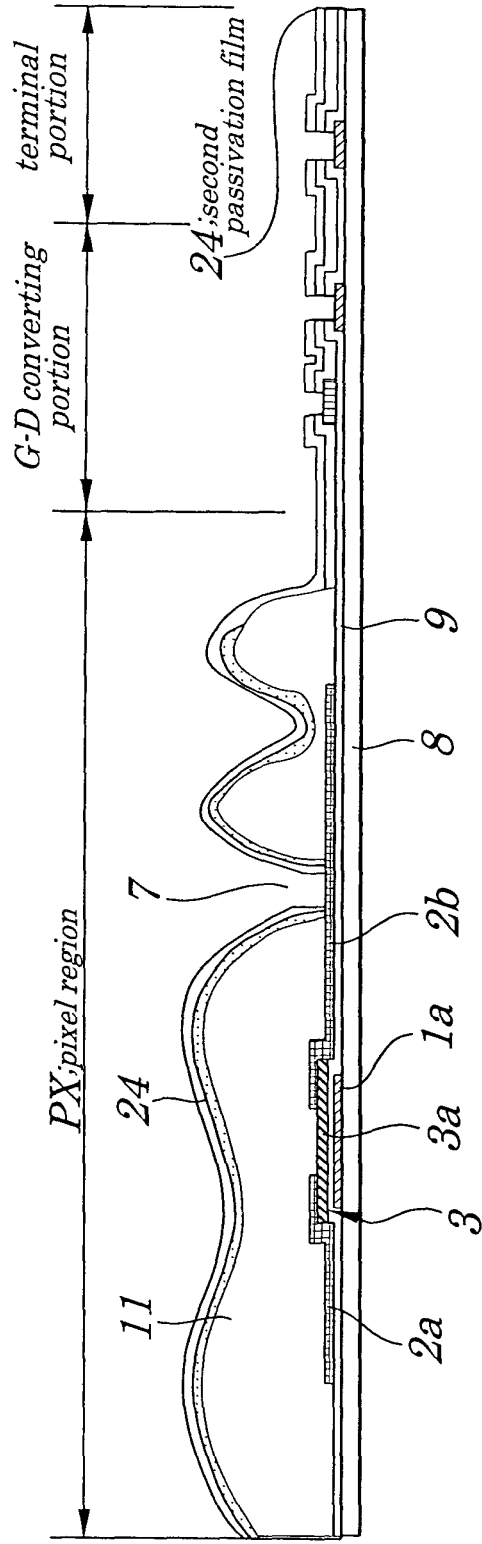
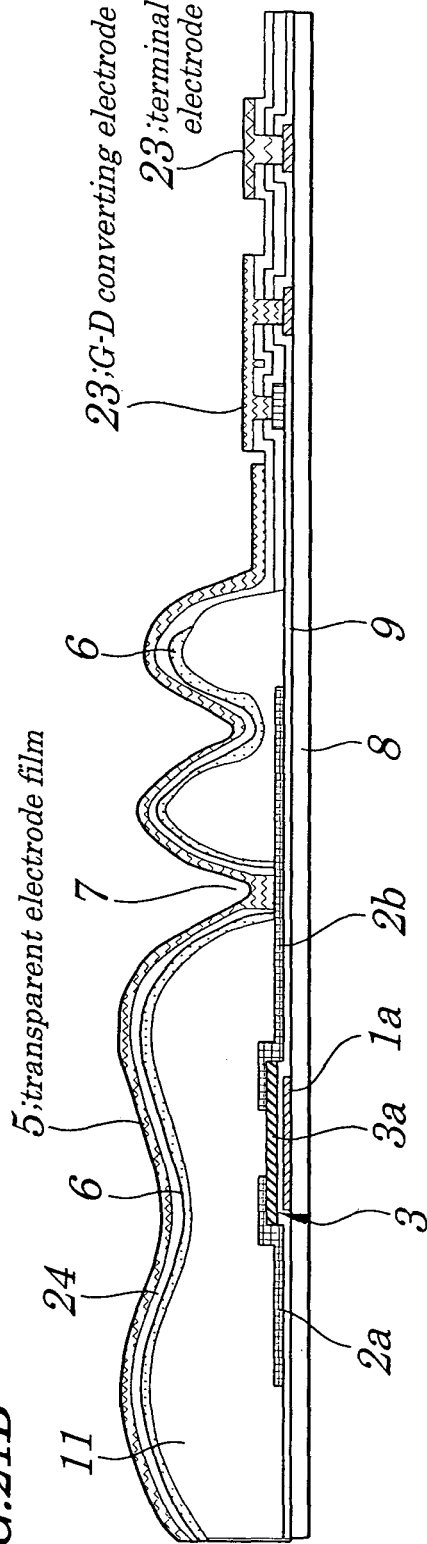


FIG. 21B



**FIG.22**

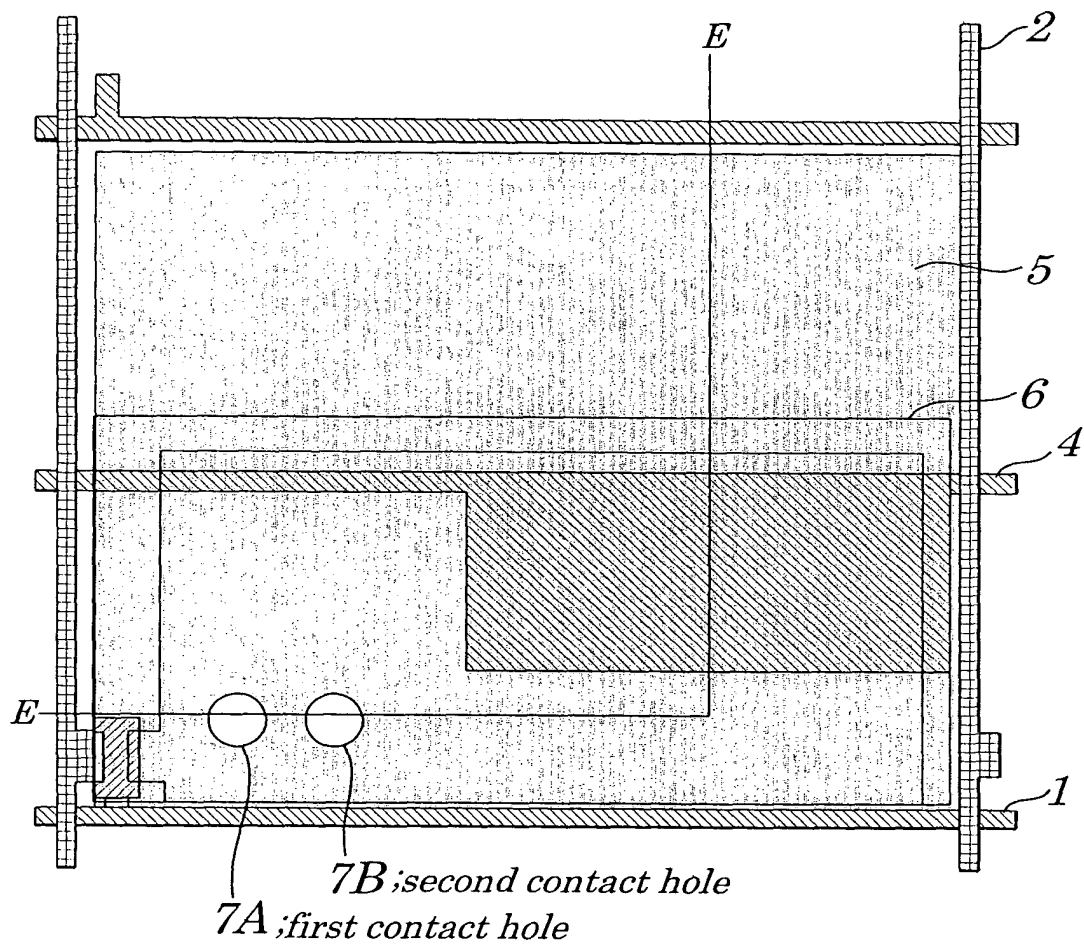
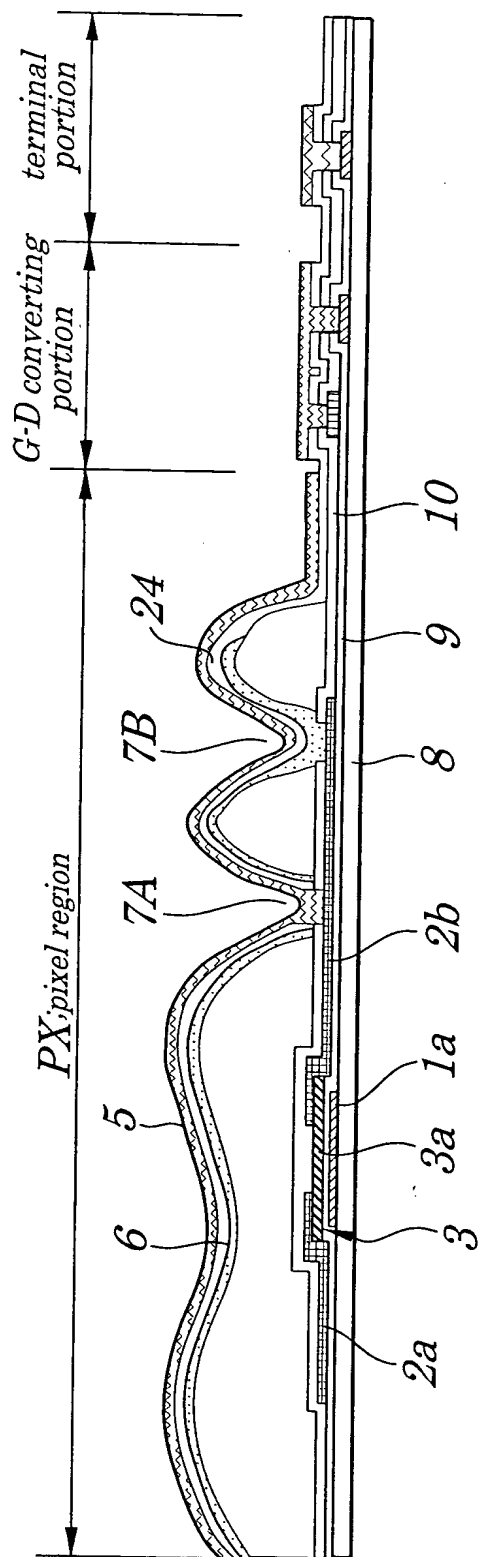


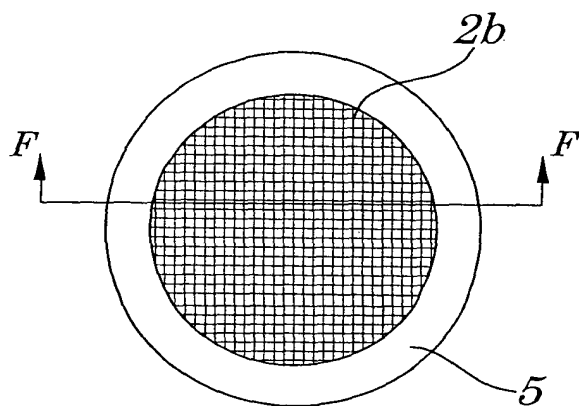
FIG.23



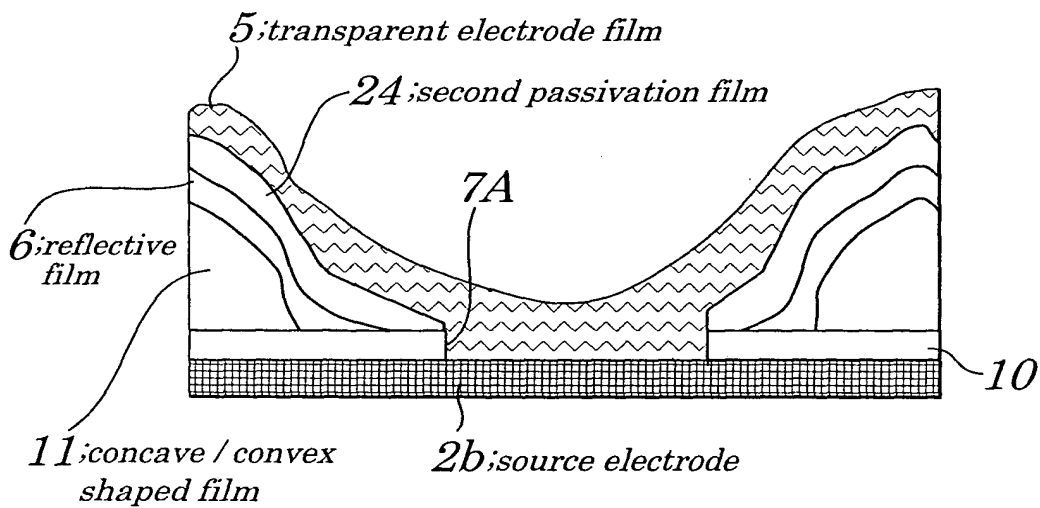


O I P E

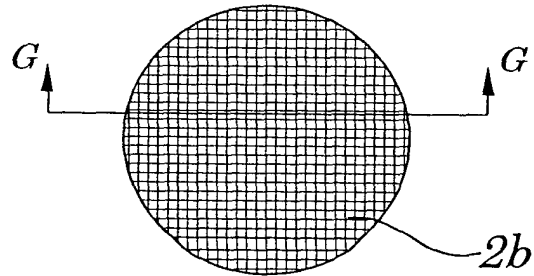
**FIG.24**



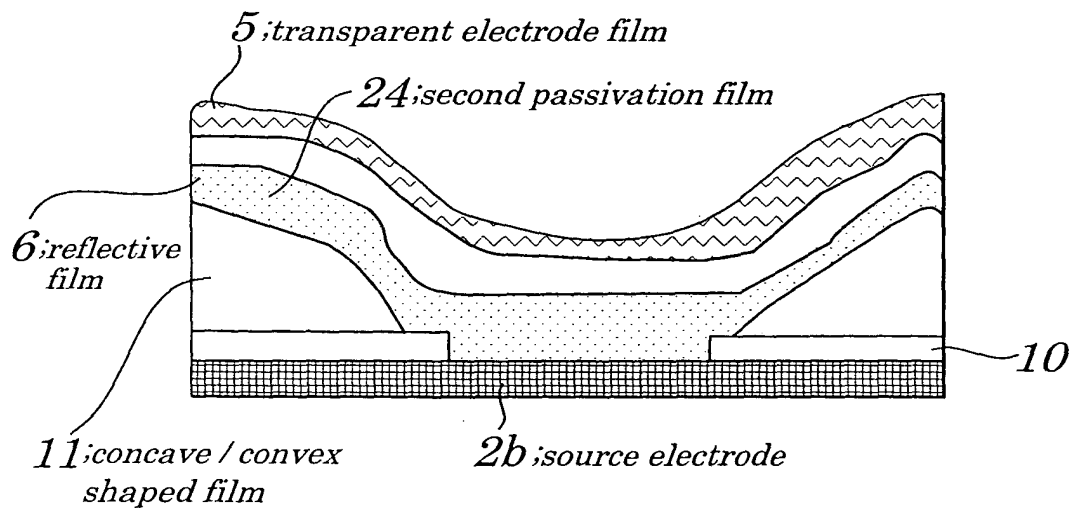
**FIG.25**



**FIG.26**



**FIG.27**



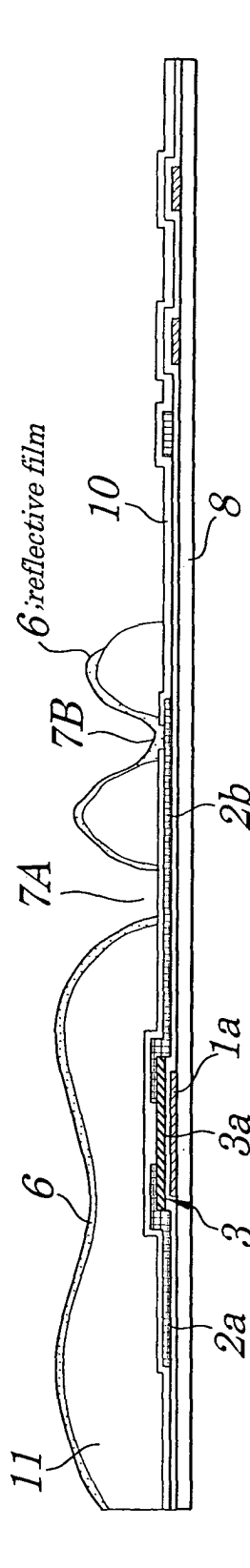
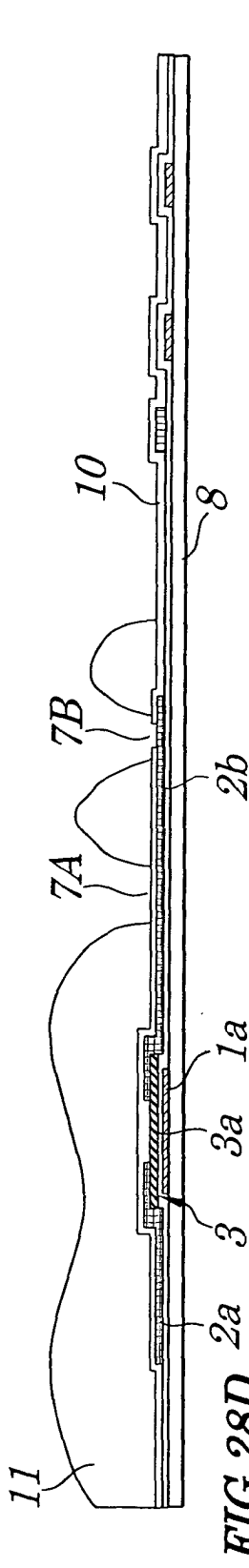
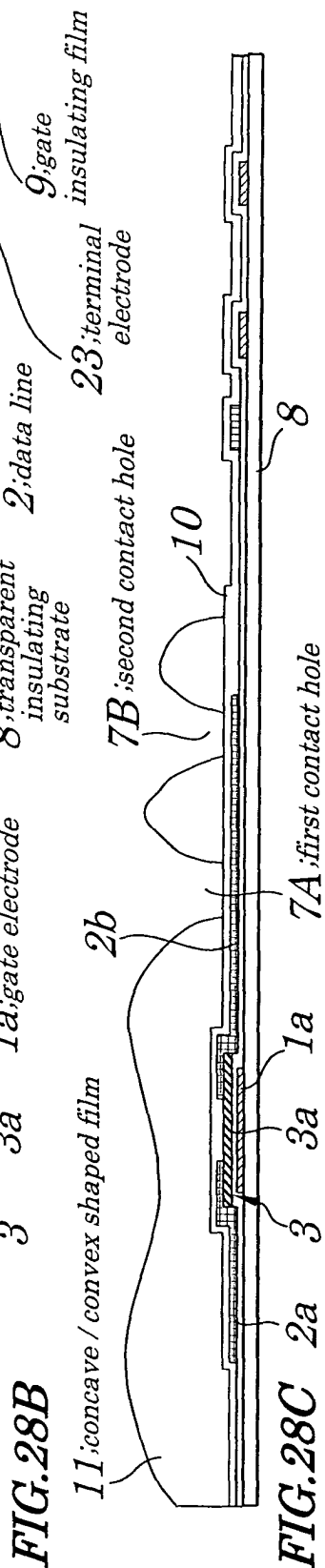
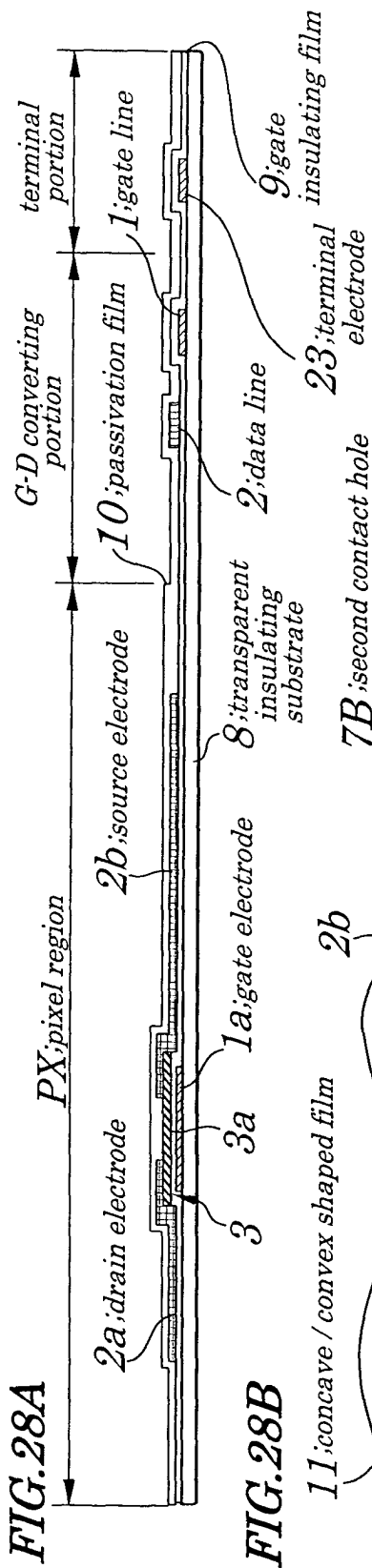




FIG. 29A

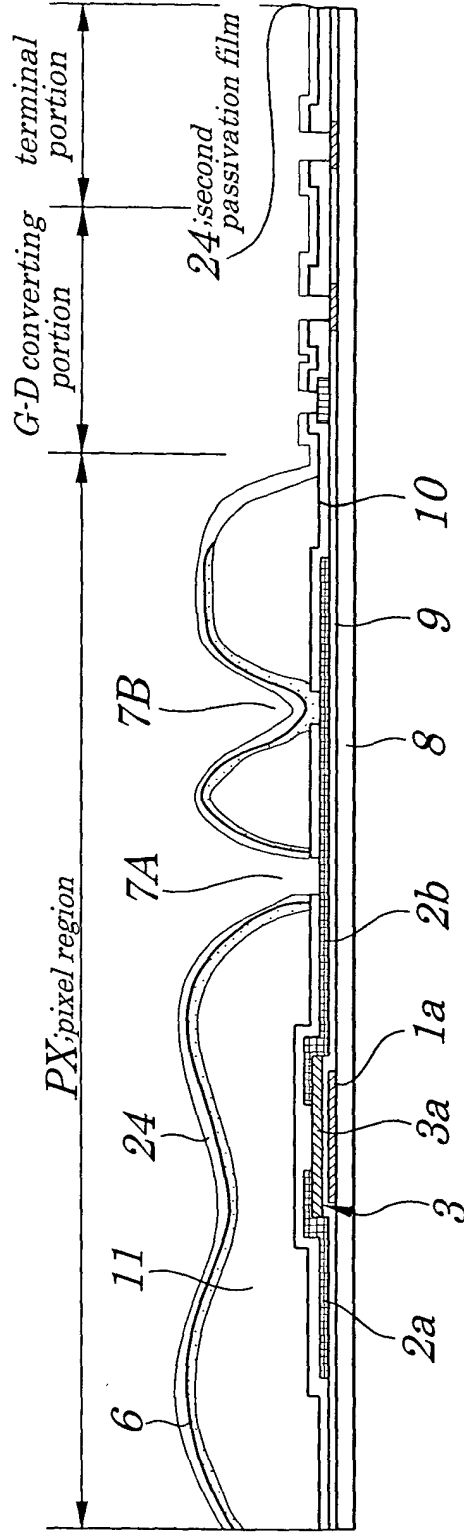


FIG. 29B

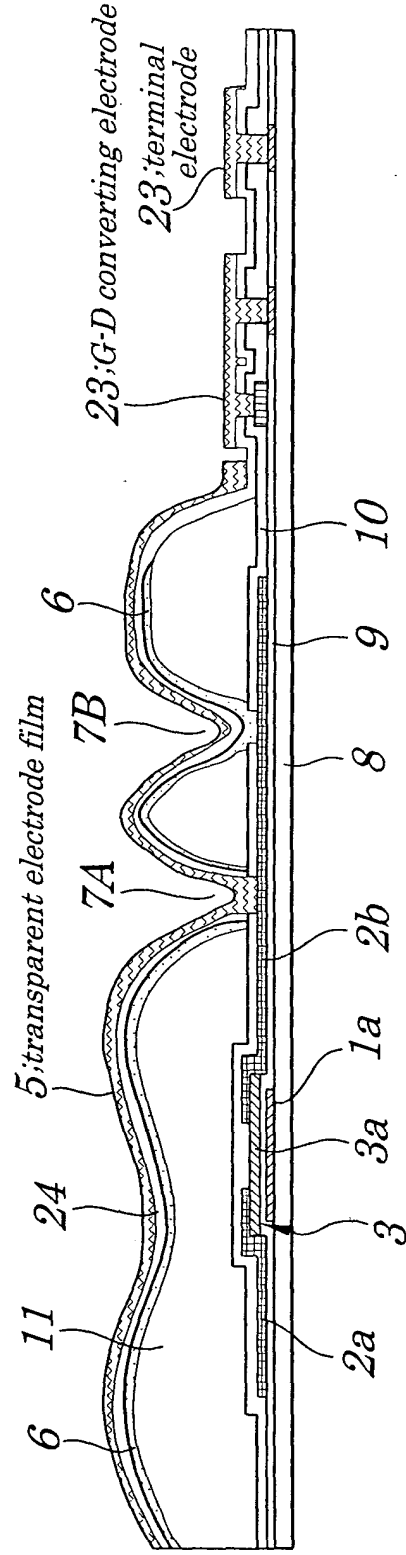
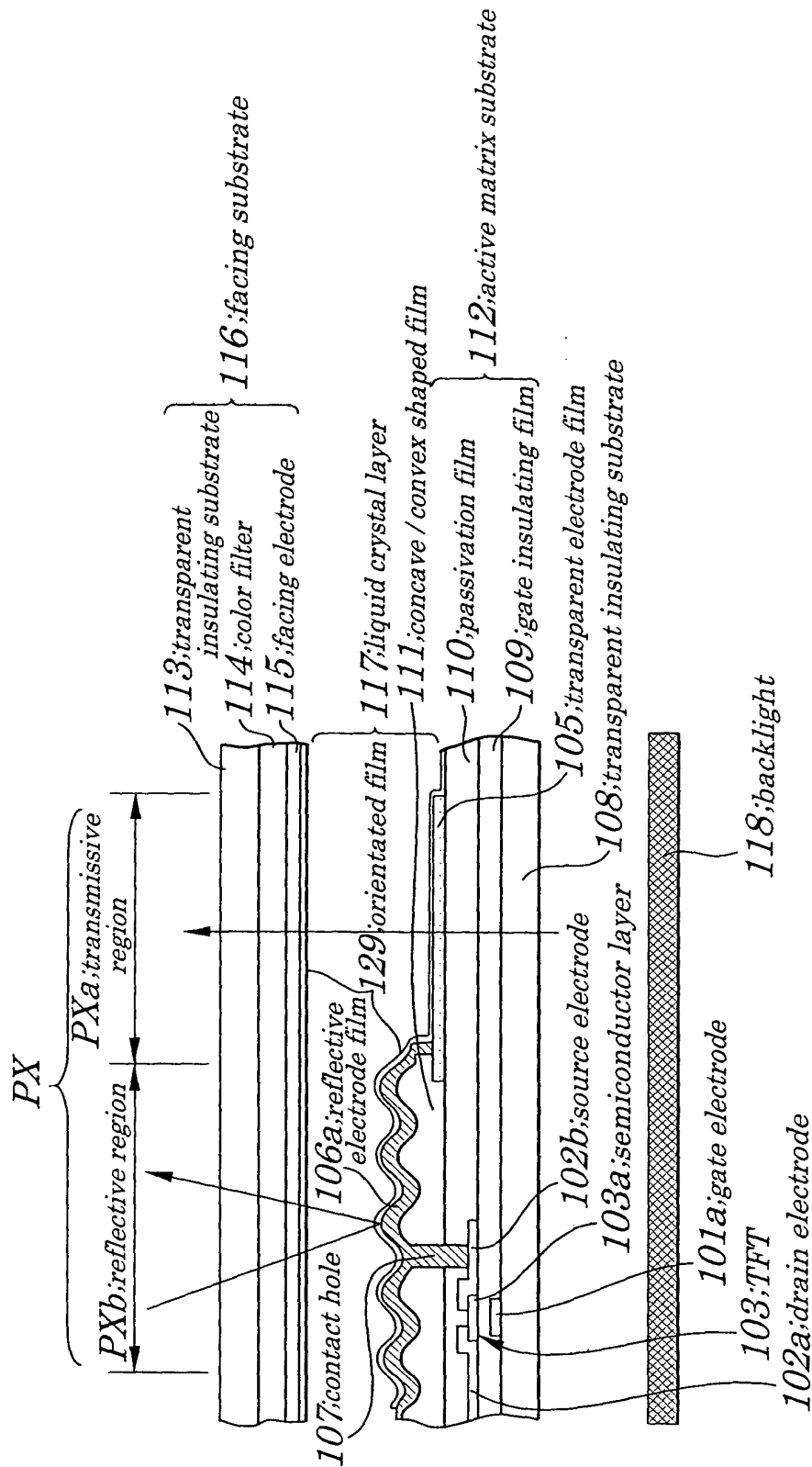
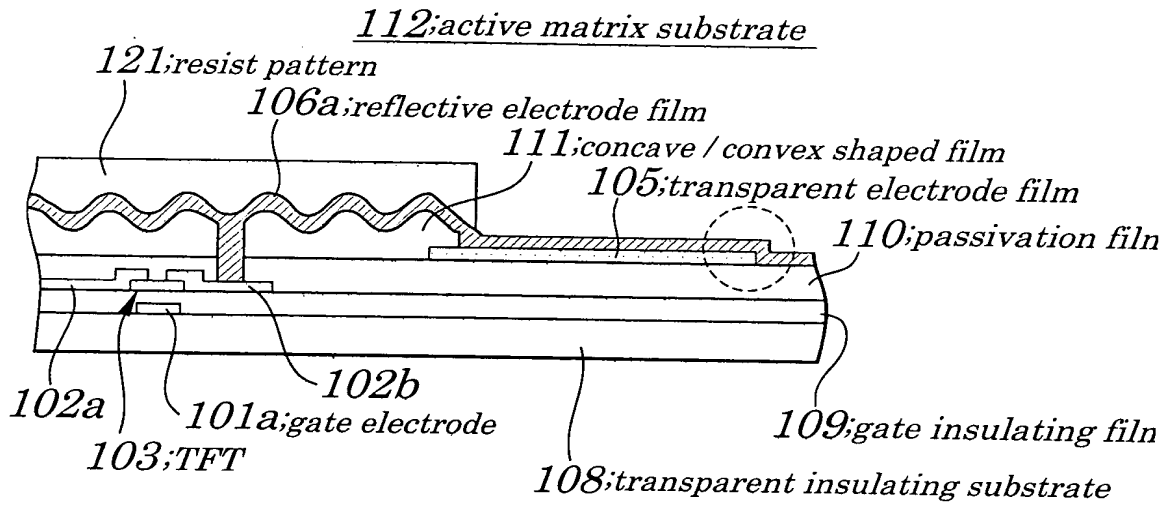


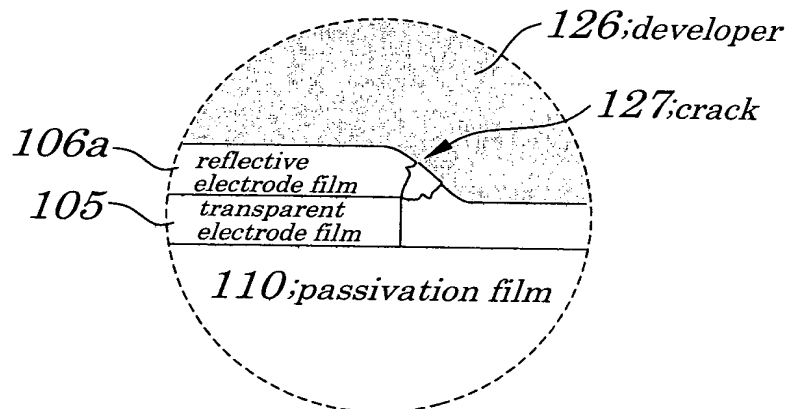
FIG.30 (PRIOR ART)



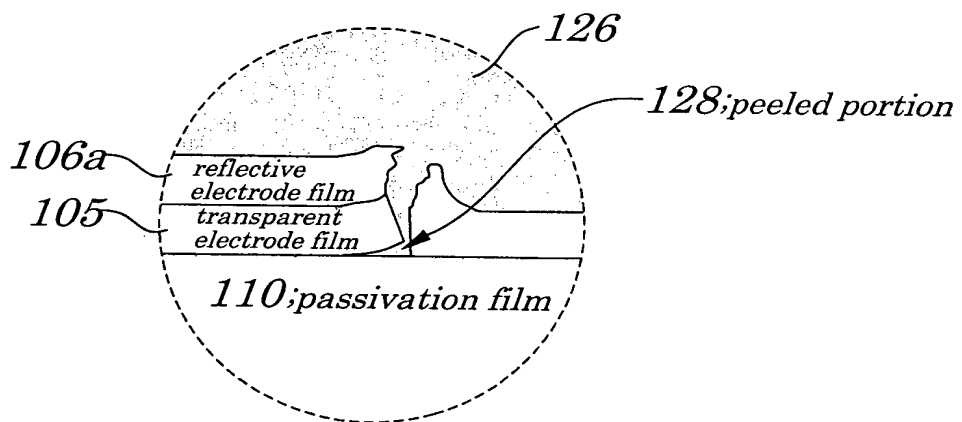
**FIG.31A(PRIOR ART)**



**FIG.31B(PRIOR ART)**



**FIG.31C(PRIOR ART)**



102; data line

105; transparent electrode film

106a; reflective electrode film

104; common storage line

102c; capacitive accumulating electrode

101; gate line

107; contact hole

104a; auxiliary capacitive electrode

103; TFT

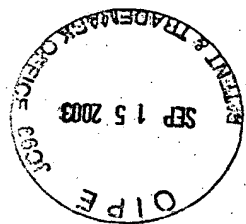


FIG. 33

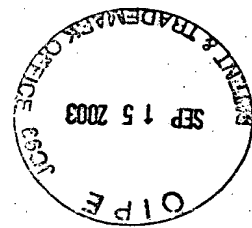
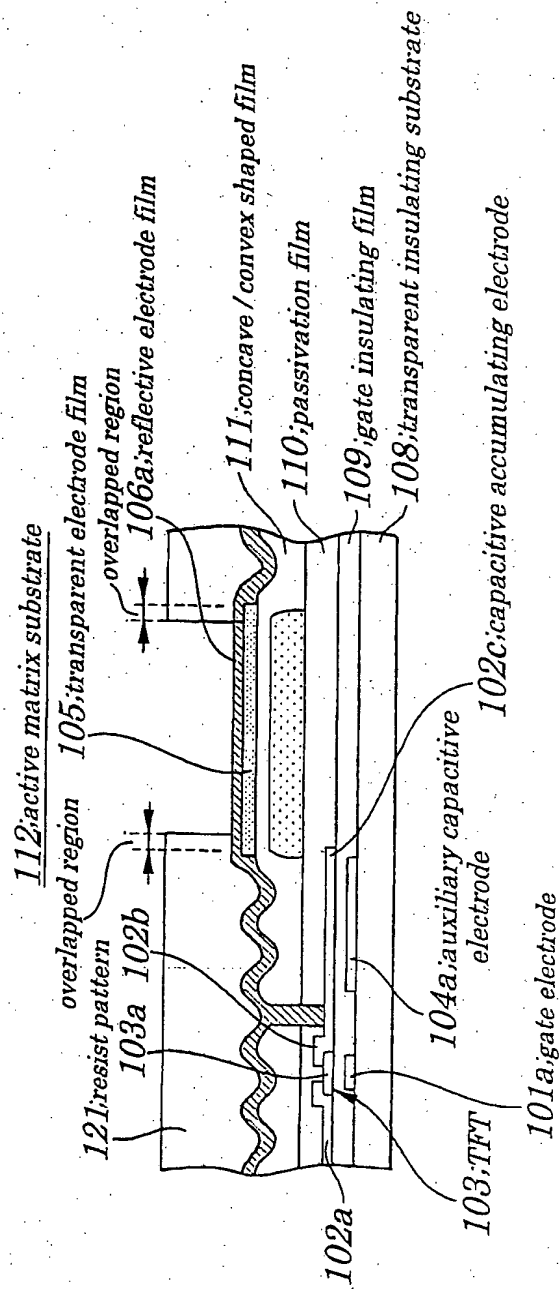
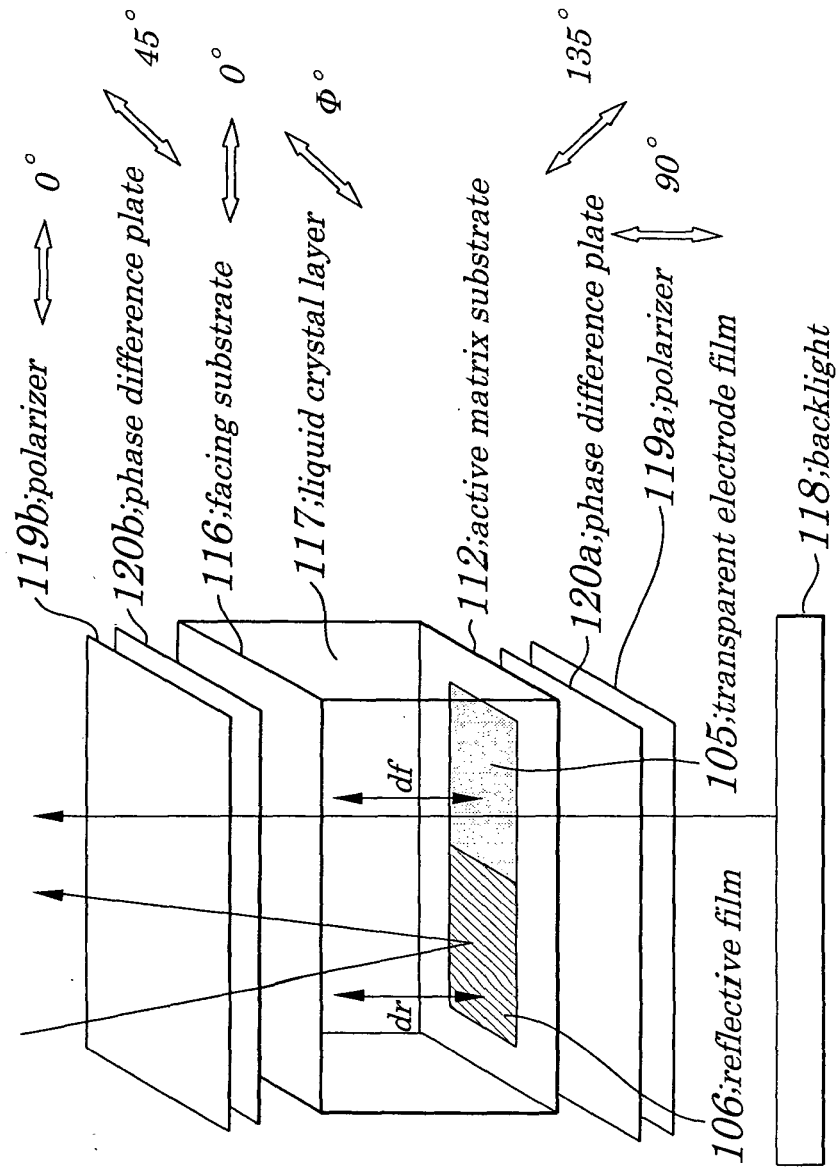
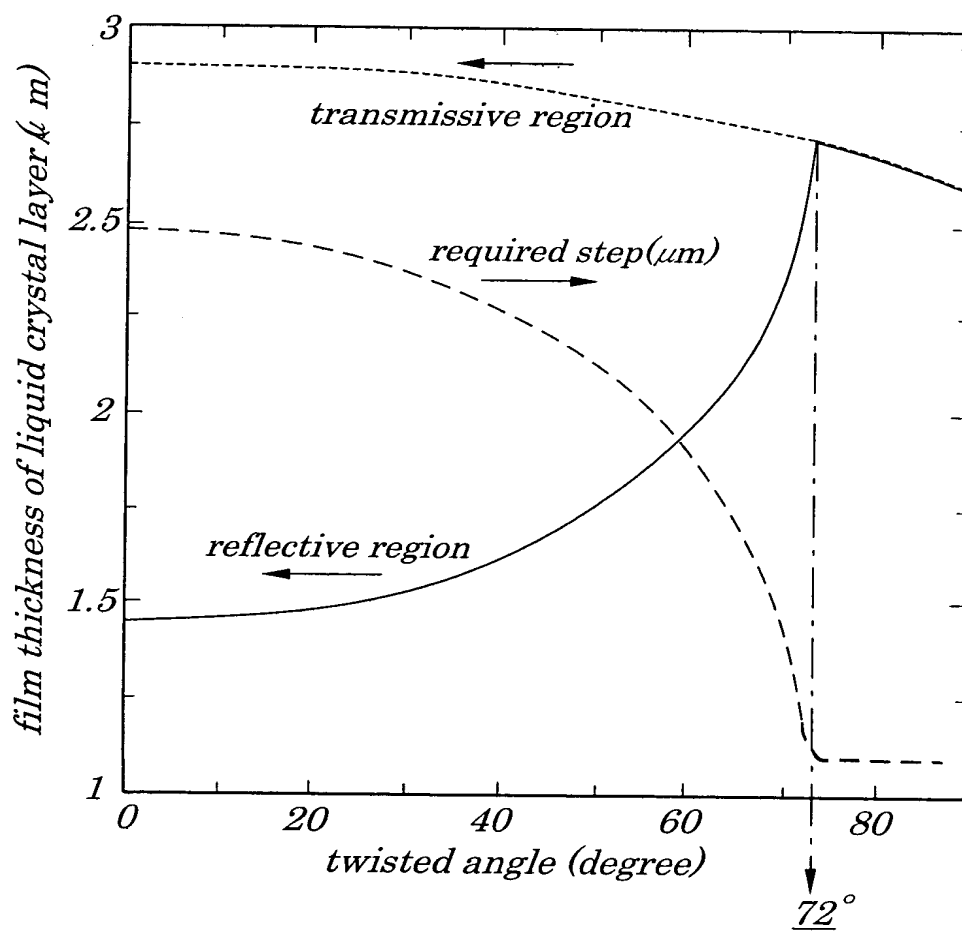


FIG. 34 (PRIOR ART)





**FIG.35 (PRIOR ART)**





***FIG.36 (PRIOR ART)***

